ANSOFT Q3D TRANING

Introduction



Quasi-static or full-wave techniques

Measure the size of the interconnect in units of wavelength!



Size $< \lambda/10$, use quasi-static solvers. Output circuit model in RLGC.

Size > $\lambda/10$, and/or radiation important, use full-wave solvers. Output S, Y, and Z parameters and fields.

Wavelength issues

low frequencies (lump model) : $\lambda/10$ wavelengths >> wire length

 $v_p = \lambda \times f$







1. 3D Fast Quasi-static EM solver



2. 2D Fast R/L/C/G EM solver





Capacitance matrix

- The equation relating the total charge on a capacitor with the potential difference relative to a ground at zero volts is : Q=CV
- In a three-conductor system, matrix notation is used:

$$\begin{bmatrix} Q_1 \\ Q_2 \\ Q_3 \end{bmatrix} = \begin{bmatrix} C_{11} & C_{12} & C_{13} \\ C_{21} & C_{22} & C_{23} \\ C_{31} & C_{32} & C_{33} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \end{bmatrix}$$



The off diagonals are always negative, which accounts for the sign of the charge on each of the conductors.

Q3D and circuit capacitance



Q3D solution:

$$\begin{bmatrix} Q_1 \\ Q_2 \end{bmatrix} = \begin{bmatrix} C_{11}^s & C_{12}^s \\ C_{21}^s & C_{22}^s \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

circuit solution:

$$Q_{1} = C_{11}^{k}V_{1} + C_{12}^{k}(V_{1} - V_{2})$$
$$Q_{2} = C_{21}^{k}(V_{2} - V_{1}) + C_{22}^{k}V_{2}$$



$$C_{11}^{s} = C_{11}^{k} + C_{12}^{k}$$
$$C_{12}^{s} = -C_{12}^{k}$$

Self-inductance

wire carrying a current



photo source: Halliday and Resnick, Physics, 1962



Mutual-inductance

• A voltage is induced across a conductor when the number of field lines around it changes.



Partial inductance

Partial self inductance: number of field lines per amp around just the conductor segment.

Partial mutual inductance: number of field lines per amp around both the conductor segment.





Partial inductance matrix

Defined for any collection of conductors

$$V_j = \sum_k L_{jk} \frac{dI_k}{dt}$$





inductance

inductance

Loop inductance



$$V_{2} = L_{22} I_{2} + L_{21} I_{1} + L_{23} I_{3} + L_{2g} I_{g}$$
$$V_{gnd} = L_{gg} I_{g} + L_{g1} I_{1} + L_{g2} I_{2} + L_{g3} I_{3}$$

$$V_2' = V_2 - V_{gnd}$$

$$= I_{1}(L_{21} - L_{2g} - L_{g1} + L_{gg})$$

+ $I_{2}(L_{22} - L_{2g} - L_{g2} + L_{gg})$
+ $I_{3}(L_{23} - L_{2g} - L_{g3} + L_{gg})$

Inductance matrix

- The individual elements of the inductance matrix are computed in the same way as the elements of the capacitance matrix.
- For a three-conductor system with a well-defined ground return path, the relationship between the magnetic flux in each loop and the current loop I in each is given by:

$$\begin{bmatrix} \Phi_1 \\ \Phi_2 \\ \Phi_3 \end{bmatrix} = \begin{bmatrix} L_{11} & L_{12} & L_{13} \\ L_{21} & L_{22} & L_{23} \\ L_{31} & L_{32} & L_{33} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \\ I_3 \end{bmatrix}$$

The diagonal elements are self-inductances and the symmetric off diagonal elements are the mutual inductances of the loops.

Solve setup

- Capacitance matrix
- DC Resistance and inductance matrix
- AC Resistance and inductance matrix

General C DC RL AC RL Defaults	1
Solution Selection	
Capacitance	
DC Resistance/Inductance	
C Resistance Only	
▼ AC Resistance/Inductance	
Use Default	

確定

取消

Solve setup



Solve setup



Mesh of DC and AC solution

DC



 AC



Reduced matrix operation

- □ Move sink
- \Box Add sink
- □ Join in series
- □ Join in parallel
- □ Float net
- Return path
- □ Ground net
- Float terminal
- □ Float at infinity
- Change frequency

<u>M</u>ove Sink... <u>A</u>dd Sink... Join In <u>S</u>eries... Join In <u>P</u>arallel... <u>F</u>loat Net... <u>G</u>round Net... Float <u>T</u>erminal... Float At <u>I</u>nfinity <u>R</u>eturn Path... <u>C</u>hange Frequency...

Move sink

Let you switch the placement of sink terminals in a conductor without having to change the terminal assignment and generate a new solution.



Add sink

- Allow user to add current sinks to a model without having to change the setup and generate a new solution.
- Allows user to simulate the presence of multiple current sinks in a conductor. While actually solving the model, only a single sink is allowed for conduction simplicity.





Join in series and parallel

This feature allows you to connect two or more conductors in series and parallel.



Ground net and Return path

- Grounded net reduce feature allows you to add grounded conductors to your model.
- Return path lets you select a conductor that is identified as a return path enabling you to model the effects of return currents on the inductance and resistance matrices.



Notice that the negative reference node for defining the branch voltages has also been changed.

Q3D extractor processes



Reference

- A. E. Ruehli, "Inductance calculations in a complex integrated circuit environment," *IBM J. Res. Develop.*, vol. 16, pp. 470-481, Sept. 1972.
- A. E. Ruehli and P. A. Brennan, "Capacitance models for integrated circuit metallization wires," *IEEE J.* Solid-State Cir., vol. SC-10, pp.530-536, Dec. 1975.